ABSTRACT OF THE DISCLOSURE

A liquid crystal display device capable of improving display quality by enabling proper execution of receipt and acceptance of image signals through compensation for variation in duty ratios of clock signals as input to liquid crystal driver circuitry, is provided. In a liquid crystal display device comprising a liquid crystal display element and liquid crystal driver circuitry, the liquid crystal driver circuitry is operable to receive an image signal as input thereto for taking it into a bus at the timing of a change of an internal clock signal from a first level to a second level or alternatively its change from the second level to the first level and then select from the image signal as taken or "accepted" into the bus a voltage used to drive the liquid crystal display element, wherein the internal clock signal is the clock signal that causes a first level period and a second level period of an external clock signal being input to the liquid crystal driver circuitry to be made identical or equalized by a clock compensation circuit to specified values respectively.